



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2829  
Examiner: VELEZ, ROBERTO

In Re PATENT APPLICATION Of:

Applicants : Toshio TERAISHI )

Appln. No. : 10 /617,817 )

Filed : July 14, 2003 )

For : ANALOG VOLTAGE OUTPUT )  
DRIVER LSI CHIP HAVING TEST )  
CIRCUIT )

Attorney Ref.: 03DCOAI030 )

AMENDMENT

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June 1, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Va 22313-1450

Sir:

In response to the Examiner's Office Action mailed December 14, 2006, which has been extended to expire on March 14, 2007 by a Petition for a three-month Extension of Time filed herewith, please amend the above-identified application as follows: